	Application No.	Applicant(s)		
	10/631,379	BVI VKDIČHVIVVI KVD.	BALAKRISHNAN, KARTHIK	
Notice of Allowability	Examiner	Art Unit		
	ANH PHUNG	2824		
The MAILING DATE of this communication appeall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RID of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comm IGHTS. This application is	in this application. If not included unication will be mailed in due cou	urse, THIS	
1. This communication is responsive to the application filed o	n July 30, 2003			
2. ☑ The allowed claim(s) is/are <u>1-30</u> .				
3. The drawings filed on <u>04 December 2003</u> are accepted by	the Examiner.			
 4. Acknowledgment is made of a claim for foreign priority under the complex of the complex of the complex of the complex of the priority documents have a complex of the priority documents have complex of the complex of the priority documents have complex of the compl	e been received.			
3. Copies of the certified copies of the priority do	cuments have been receive	ed in this national stage application	n from the	
International Bureau (PCT Rule 17.2(a)).				
* Certified copies not received:				
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE		e a reply complying with the requi	rements	
5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give			ICE OF	
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submited.			
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Revie	ew (PTO948) attached		
1) ☐ hereto or 2) ☐ to Paper No./Mail Date				
(b) including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment o	or in the Office action of		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the	• • •	- · · · · · · · · · · · · · · · · · · ·	ck) of	
 DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT 			the	
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5 C Notice of Ir	nformal Patent Application (PTO-15	52)	
2. ☐ Notice of References Cited (F10-032) 2. ☐ Notice of Draftperson's Patent Drawing Review (PT0-948)	-	ummary (PTO-413),	<i>32</i> j	
2. Thouse of Branperson's Faterit Brawing Review (1 10040)	Paper No.	/Mail Date		
 Information Disclosure Statements (PTO 1449 or PTO/SB/0 Paper No./Mail Date 		Amendment/Comment		
4. Examiner's Comment Regarding Requirement for Deposit		Statement of Reasons for Allowa	nce	
of Biological Material	9. ⊠ Other <u>Sear</u>			
	Ch	ch Theene		
		ANH PHUNG IMARY EXAMINER		

Detailed Action

1. In response to the U.S. Application Ser. No. 10/631,379 filed on July 30, 2003, claims **1-30** are pending in the application.

Examiner's Statement of Reasons for Allowance

2. Claims **1-30** are allowed.

Ouellette et al. (US 6,333,872), Wen (US 6,166,963), Gibbins et al. (US 5,742,557), Chung et al. (US 5,590,087) and Aoki (US 5,493,536) disclose a semiconductor device and associated method, having a memory structure and a read circuit similar to that of the present application, but fail to teach:

"a corruption prevention circuit operatively coupled to said at least one write word line and said memory cell structure such that when a read operation is enabled for said at least one memory cell structure during a write operation for said at least one memory cell structure, said corruption prevention circuit forces said at least one read bit line to a known digital value" as claimed in the independent claims 1 and 10; or

"operatively coupling a corruption prevention circuit to said at least one write word line and said memory cell structure such that when a read operation is enabled for said at least one memory cell structure during a write operation for said at least one memory cell structure, said corruption prevention circuit forces said at least one read bit line to a known digital value" as claimed in the independent claim 22.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably Application/Control Number: 10/631,379 Page 3

Art Unit: 2824

accompany the issue fee. Such submissions should be clearly labeled "Comment on Statement of Reasons for Allowance".

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-1883**. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD ELMS, can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Art Unit: 2824

AKP

ANH PHUNG
PRIMARY EXAMINER